

## Computer Architecture Final Exam Solutions

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CS 152 Computer Architecture and Engineering Final Exam SOLUTIONS May 12, 2020 Professor Krste Asanovi Name: \_\_\_\_\_ SID: \_\_\_\_\_ I am taking CS152 / CS252 (circle one) 180 Minutes, 27 pages. Notes: • Not all questions are of equal difficulty, so look over the entire exam!

CS 152 Computer Architecture and Engineering Final Exam ...

Computer Architecture Final Exam Solutions CSE 30321 – Computer Architecture I – Fall 2010 Final Exam December 13, 2010 Test Guidelines: 1. Place your name on EACH page of the test in the space provided. 2. Answer every question in the space provided. If separate sheets are needed,

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Computer Architecture - Instructions 1. Write your name now. Name: Final Exam A simple (No wireless) calcu- 2. 3. 4. 5. 6. 8. Maximum score: 115. This exam is open book and open notes. lator is allowed. You should have 6 questions in 16 pages. Exam length: 2 hours. Read all the questions before you begin. Show all

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your work. Write clearly.

FINAL EXAM3 - Department of Electrical & Computer Engineering

EE321: Computer Architecture Final Exam Solution (1h30) 2017 – 2018 Notes : Answer briefly and clearly using the provided space. No extra sheet will be accepted. 1. Interrupts (5 pts) Consider a system with 3 I/O devices: D1, D2, and D3, with increasing priorities of 1 (low priority), 2 and 3 (high priority), respectively.

EE321: Computer Architecture Final Exam Solution (1h30 ...

CSE 30321 – Computer Architecture I – Fall 2010 Final Exam December 13, 2010 Test Guidelines: 1. Place your name on EACH page of the test in the space provided. 2. Answer every question in the space provided. If separate sheets are needed, make sure to include your name and clearly identify the problem being solved. 3. Read each question ...

CSE 30321 – Computer Architecture I – Fall 2010 Final Exam ...

Computer Architecture 1 Fall 2011 Final Exam Solutions, Uppsala University Page 3 of 8 MIPS&Assembly&[12points]& 5a. Write MIPS assembly for the algorithm below. [4 points]  $c = a + b + 4$ ; `do { c -= a; b++; } while (c > 3)` a is in \$t0, b is in \$t1, c is in \$s0. Clear comments are required for each line of code. Code Comments

Dark 1 HT2011 Exam Solutions - Uppsala University

Computer Architecture MCQ Question with Answer Computer Architecture MCQ with detailed explanation for interview, entrance and competitive exams. Explanation are given for understanding. Download Computer Architecture MCQ Question Answer PDF

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CSE 490/590 Computer Architecture Midterm Solution DIRECTIONS Time limit: 45 minutes (12pm - 12:45pm) There are 40 points plus 5 bonus points. This is a closed-book, no calculator, closed-notes exam. Each problem starts on a new page. Please use a pen, not a pencil. If you use a pencil, it won't be considered for regrading.

CSE 490/590 Computer Architecture Midterm Solution

EXAM 1 SOLUTIONS Assume we would like to use the exact same solution (history buffer) for executing a store instruction to memory. Why is this difficult to do? It is difficult to UNDO the effect of a store instruction. Another processor might read and use the value supplied by the store instruction before the store is undone.

EXAM 1 SOLUTIONS - Electrical and Computer Engineering

This means that the computer (CPU) will seem faster to the user with more RAM to keep more data and instructions close at hand for the CPU. Hardware D 19 Yes. Thumb Drives (a.k.a. USB keys) offer more storage and quicker data access in a smaller, more convenient package. Most PC makers have stopped including

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diskette drives as standard PC ...

Answers to Chapters 1,2,3,4,5,6,7,8,9 - End of Chapter ...

Title: Dark 1 HT2011 Exam Solutions Author: David Black-Schaffer Created Date: 1/6/2012 12:07:31 PM

Dark 1 HT2011 Exam Solutions - Uppsala University

Reading: Essentials of Computer Architecture Chapter 18. Question and Answer session, catch-up, review, or topic of your choice. Please email suggestions.

Course review, prepare for final exam (a draft solutions). Assignment 5 Solutions; Assignment and Lab schedule on WebCT. Final exam (draft solutions),

Monday, April 30, 2007, 19:00-21:00, SC 239.

CS 250: Computer Architecture

18-447 Intro to Computer Architecture, Spring 2012 Final Exam Instructor: Onur Mutlu Teaching Assistants: Chris Fallin, Lavanya Subramanian, Abeer Agrawal

Date: May 10, 2012 Name: SOLUTIONS Problem I (190 Points) : Problem II (60 Points) : Problem III (60 Points) : Problem IV (30 Points) : Problem V (45 Points) : Problem VI (60 Points) :

Name: SOLUTIONS - Electrical and Computer Engineering

CMSC411 Fall 2009 Final Exam Solution 1. (28 pts) Architectures a. Give an example of an area of computer architecture where bandwidth has improved faster than latency. How has this gap affected performance? Memory, storage, networks, etc. b. Describe how speculation can improve performance where dynamic scheduling cannot.

CMSC411 Fall 2009 Final Exam Solution

EEL 4713 – Computer Architecture Final Exam Friday, May 5th, 2006 NAME: Please read each question carefully, to avoid any confusion. This exam should have a total of 14 pages printed double-sided (pages 13 and 14 are scratch space). Before you begin, make sure your copy contains all pages. The exam is closed book, closed notes. Each question ...

EEL 4713 – Computer Architecture Final Exam

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Midterm 2 will be held in EHxxxx from 10:05am-12:05pm during the final exam slot on Monday, Dec. 16, 2019; Course Project Information. For the course project you will do a research-focused project. This may involve reimplementing an idea proposed in a paper we read in the class (or outside of class) or trying something new that you have come up ...

ECE/CS 752 Fall 2019

View full document Team Project 02 This is the Coursera Computer Architecture Course Final Exam Solution. To prepare for this assignment, the team must divide the viewing and analyzing of the Coursera ' s Princeton University Computer Architecture classes (weeks 3 to 11).

Computer Architecture Multiple Choice Questions and Answers (MCQs): Quizzes & Practice Tests with Answer Key (Computer Architecture Quick Study Guide & Course Review) covers course assessment tests for competitive exams to solve 750 MCQs. "Computer Architecture MCQ" with answers covers fundamental concepts with theoretical and analytical reasoning tests. "Computer Architecture Quiz" PDF study guide helps to practice test questions for exam review. "Computer Architecture Multiple Choice Questions and Answers" PDF book to download covers solved quiz questions and answers PDF on topics: Assessing computer performance, computer architecture and organization, computer arithmetic, computer language and instructions, computer memory review, computer technology, data level parallelism and GPU architecture, embedded systems, exploiting memory, instruction level parallelism, instruction set principles, interconnection networks, memory hierarchy design, networks, storage and peripherals, pipelining in computer architecture, pipelining performance, processor datapath and control, quantitative design and analysis, request level and data level parallelism, storage systems, thread level parallelism for college and university level exams. "Computer Architecture Questions and Answers" PDF covers exam's viva, interview questions and certificate exam preparation with answer key. Computer architecture quick study guide includes terminology definitions in self-teaching guide from computer science textbooks on chapters: Assessing Computer Performance MCQs Computer Architecture and Organization MCQs Computer Arithmetic MCQs Computer Language and Instructions MCQs Computer Memory Review MCQs Computer Technology MCQs Data Level Parallelism and GPU Architecture MCQs Embedded Systems MCQs Exploiting Memory MCQs Instruction Level Parallelism MCQs Instruction Set Principles MCQs Interconnection Networks MCQs Memory Hierarchy Design MCQs Networks, Storage and Peripherals MCQs Pipelining in Computer Architecture MCQs Pipelining Performance MCQs Processor Datapath and Control MCQs Quantitative Design and Analysis MCQs Request Level and Data Level Parallelism MCQs Storage Systems MCQs Thread Level Parallelism MCQs Multiple choice questions and answers on assessing computer performance MCQ questions PDF covers topics: Introduction to computer performance, CPU performance, and two spec benchmark test. Multiple choice questions and answers on computer architecture and organization MCQ questions PDF covers topics: Encoding an instruction set, instruction set operations, and role of compilers. Multiple choice questions and answers on computer arithmetic MCQ questions PDF covers topics: Addition and subtraction, division calculations, floating point, ia-32 3-7 floating number, multiplication calculations, signed, and unsigned numbers. Multiple choice questions and answers on computer language and instructions MCQ questions PDF covers topics: Computer instructions representations, 32 bits MIPS addressing, arrays and pointers, compiler optimization, computer architecture, computer code, computer hardware operands, computer hardware operations, computer hardware procedures, IA 32 instructions, logical instructions, logical operations, MIPS fields, program translation, sorting program. Multiple choice questions and answers on computer memory review MCQ questions PDF covers topics: Memory hierarchy review, memory technology review, virtual memory, how virtual memory works, basic cache optimization methods, cache optimization techniques, caches performance,

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computer architecture, and six basic cache optimizations. Multiple choice questions and answers on computer technology MCQ questions PDF covers topics: Introduction to computer technology, and computer instructions and languages. Multiple choice questions and answers on data level parallelism and GPU architecture MCQ questions PDF covers topics: Loop level parallelism detection, architectural design vectors, GPU architecture issues, GPU computing, graphics processing units, SIMD instruction set extensions, and vector architecture design. Multiple choice questions and answers on embedded systems MCQ questions PDF covers topics: Introduction to embedded systems, embedded multiprocessors, embedded applications, case study SANYO vpc-sx500 camera, and signal processing. Multiple choice questions and answers on exploiting memory MCQ questions PDF covers topics: Introduction of memory, virtual memory, memory hierarchies framework, caches and cache types, fallacies and pitfalls, measuring and improving cache performance, Pentium p4 and AMD Opteron memory. Multiple choice questions and answers on instruction level parallelism MCQ questions PDF covers topics: Instruction level parallelism, ILP approaches and memory system, limitations of ILP, exploiting ILP using multiple issue, advanced branch prediction, advanced techniques and speculation, basic compiler techniques, dynamic scheduling algorithm, dynamic scheduling and data hazards, hardware based speculation, and intel core i7. Multiple choice questions and answers on instruction set principles MCQ questions PDF covers topics: Instruction set architectures, instruction set operations, computer architecture, computer code, memory addresses, memory addressing, operands type, and size. Multiple choice questions and answers on interconnection networks MCQ questions PDF covers topics: Interconnect networks, introduction to interconnection networks, computer networking, network connectivity, network routing, arbitration and switching, network topologies, networking basics, and switch microarchitecture. Multiple choice questions and answers on memory hierarchy design MCQ questions PDF covers topics: Introduction to memory hierarchy design, design of memory hierarchies, cache performance optimizations, memory technology and optimizations, and virtual machines protection. Multiple choice questions and answers on networks, storage and peripherals MCQ questions PDF covers topics: Introduction to networks, storage and peripherals, architecture and networks, disk storage and dependability, I/O performance, reliability measures, benchmarks, I/O system design, processor, memory, and I/O devices interface. Multiple choice questions and answers on pipelining in computer architecture MCQ questions PDF covers topics: Introduction to pipelining, pipelining implementation, implementation issues of pipelining, pipelining crosscutting issues, pipelining basic, fallacies and pitfalls, major hurdle of pipelining, MIPS pipeline, multicycle, MIPS R4000 pipeline, and intermediate concepts. Multiple choice questions and answers on pipelining performance MCQ questions PDF covers topics: What is pipelining, computer organization, pipelined datapath, and pipelining data hazards. processor datapath and control MCQ questions PDF covers topics: datapath design, computer architecture, computer code, computer organization, exceptions, fallacies and pitfalls, multicycle implementation, organization of Pentium implementations, and simple implementation scheme. Multiple choice questions and answers on quantitative design and analysis MCQ questions PDF covers topics: Quantitative design and analysis, quantitative principles of computer design, computer types, cost trends and analysis, dependability, integrated circuits, power and energy, performance and price analysis, performance measurement, and what is computer architecture. Multiple choice questions and answers on request level and data level parallelism MCQ questions PDF covers topics: Thread level parallelism, cloud computing, google warehouse scale, physical infrastructure and costs, programming models, and workloads. Multiple choice questions and answers on storage systems MCQ questions PDF covers topics: Introduction to storage systems, storage crosscutting issues, designing and evaluating an I/O system, I/O performance, reliability measures and benchmarks, queuing theory, real faults, and failures. Multiple choice questions and answers on thread level parallelism MCQ questions PDF covers topics: Thread level parallelism, shared memory architectures, GPU architecture issues, distributed shared memory and coherence, models of memory consistency, multicore processors and performance, symmetric shared memory multiprocessors, and synchronization basics.

Our 1500+ Computer Architecture Questions and Answers focuses on all areas of Computer Architecture subject covering 100+ topics in Computer

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division calculations, dynamic scheduling algorithm, dynamic scheduling and data hazards, embedded multiprocessors, encoding an instruction set, exceptions, exploiting ilp using multiple issue, fallacies and pitfalls, floating point, google warehouse scale, GPU architecture issues. Computer architecture objective questions and answers on GPU computing, graphics processing units, hardware based speculation, how virtual memory works, i/o performance.

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