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instruction set by Alexander Popov.

Quick reference: AAA AAD AAM AAS
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JBE JC ... assembler generates
several RCL xx, 1 instructions
because 8086 has machine code

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Most processors have a “compare” instruction. look at the instruction reference for whatever processor you’re trying to program, and use the instruction you find. compare two operands (cmp) `cmp{bwl} reg[8|16|32], r/m[8|16]` cmp is typically executed in

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conjunction with conditional jumps and the setcc instruction. example.

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example: mov al, 5 mov bl, 5 cmp al, bl ; 1 instructions because 8086 has machine code only for this instruction
lea instruction in 8086 ...

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- The 8086 microprocessor supports 8 types of instructions ? ... STI ? Used to set the interrupt enable flag to 1, i.e., enable INTR input. CLI ? Used to clear the interrupt enable flag to 0, i.e.,

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disable INTR input. Iteration Control
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